

# Efficient Elimination of False Positives using Static Analysis

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# Background

- ▶ Static analysis - scalable but imprecise
- ▶ Model checking - precise but not scalable
- ▶ Static analysis + model checking  $\Rightarrow$  better results
- ▶ False positives elimination using model checking<sup>1</sup>
  - ▶ Generate an assertion corresponding to each warning
  - ▶ Verification in incremental context
  - ▶ Scalable to some extent but a lot many model checking calls
  - ▶ Time consuming

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<sup>1</sup>Hendrik Post et al. "Reducing False Positives by Combining Abstract Interpretation and Bounded Model Checking". In: *ASE, 2008*, pp. 188-197.

## A Motivating Example

```
1  const int arr[]
2      = {0, 2, 5, 9, 14};
3  int var, factor; char ch;
4
5  void f1(){
6      unsigned int i, j;
7
8      i = lib1();
9      j = lib2();
10     var = lib3();
11
12     if(j < 5 && i < j){
13         factor = arr[j] - arr[i];
14         f2();
15     }
16 }
```

```
21  int f2(){
22     if(var == factor )
23         f3(var);
24     ...
25 }
```

```
31  int f3(int p){
32     int a, b, denom = 1;
33     if(ch < 5)
34         denom = p;
35     else
36         denom = 10;
37
38     assert(denom != 0);
39     a = 100/denom; //warning
40 }
```

# Context Expansion: Call 1

Verifying the assertion  
in the context of *f3*  
results in

Counterexample  
by assigning 0 to *p*

```
p = nondet_int();  
ch = nondet_char();
```

```
31 int f3(int p){  
32     int a, b, denom=1;  
33     if(ch < 5)  
34         denom = p;  
35     else  
36         denom = 10;  
37  
38     assert(denom!=0);  
39     a = 100/denom;//warning  
40 }
```

## Context Expansion: Call 2

```
var = nondet_int();  
factor = nondet_int();  
ch = nondet_char();
```

Verifying the assertion  
in the context of *f2*  
results in

**Counterexample**

by assigning 0 to *var* and  
*factor*

```
21 int f2(){  
22     if(var==factor )  
23         f3(var);  
24     ...  
25 }
```

```
31 int f3(int p){  
32     int a, b, denom=1;  
33     if(ch < 5)  
34         denom = p;  
35     else  
36         denom = 10;  
37  
38     assert(denom!=0);  
39     a = 100/denom;//warning  
40 }
```

## Context Expansion: Call 3

Assertion holds when verified in the context of *f1*.

```
ch = nondet_char();
```

```
1  const int arr[]
2      ={0,2,5,9,14};
3
4  void f1(){
5      unsigned int i, j;
6
7      i = lib1();
8      j = lib2();
9      var = lib3();
10
11     if(j < 5 && i < j){
12         factor= arr[j]-arr[i];
13         f2();
14     }
15 }
```

```
21  int f2(){
22     if(var==factor )
23         f3(var);
24     ...
25 }
```

```
31  int f3(int p){
32     int a, b, denom=1;
33     if(ch < 5)
34         denom = p;
35     else
36         denom = 10;
37
38     assert(denom!=0);
39     a = 100/denom;//warning
40 }
```

# Motivation

- ▶ The problem
  - ▶ Large number of model checking calls
  - ▶ Hence, time consuming
  
- ▶ Observation
  - ▶ If any arbitrary value is allowed for an assertion variable at the assertion point, the assertion verification results in a counterexample.

# Our Approach

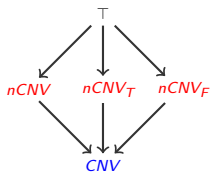
- ▶ Complete-range Non-deterministic Value (cnv) variables
  - ▶ Taking complete-range of non-deterministic values
  - ▶ Any arbitrary value is allowed
  - ▶ No value assignment/restriction through program code
- ▶ Complete-range Non-deterministic Value (cnv) expressions
  - ▶  $(x + 10)$ ,  $(x++)$ , and  $(x + y)$  **are** *cnv* expressions when  $\{x, y, z\}$  are *cnv* variables.
  - ▶  $(x + 10)$ , and  $(x++)$  **are not** *cnv* expressions when  $\{y, z\}$  are *cnv* variables.
  - ▶  $(x/100)$ ,  $(x\%2)$ , and  $(100)$  in *n* **are not** *cnv* expressions even if  $x$  is a *cnv* variable.
- ▶ Identify redundant verification calls (RVCs) and skip them
  - ▶ using *cnv* variables



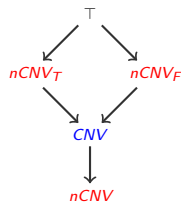
# Computation of cnv variables

- ▶ Depends on
  - ▶ Context sensitivity
  - ▶ Flow sensitivity
  - ▶ May/Must reachability
  - ▶ Data and **control** dependance
- ▶ Using Data Flow Analysis
  - ▶ Lattice structure

```
1 void f(int x){
2     //x → CNV
3     if(x < 10){
4         //x → nCNVT
5     }else{
6         //x → nCNVF
7     }
8     //x → CNV
9 }
```



(a) May cnv variables



(b) Must cnv variables

# RVCs Identification

If all assertion variables are *cnv* variables, the call is redundant.

```
1  const int arr[]
2      ={0,2,5,9,14};
3  int var, factor; char ch;
4
5  void f1(){
6      unsigned int i, j;
7
8      i = lib1();
9      j = lib2();
10     var = lib3();
11
12     if(j < 5 && i < j){
13         factor= arr[j]-arr[i];
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```
21  int f2(){
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38     assert(denom!=0);
39     a = 100/denom;//warning
40 }
```

# Influence of *cnv* variables

```
1 void foo(){
2   b = 0;
3   if(v == 1){
4     a = 10;
5     b = 10;
6   }
```

```
11 if(v == 1)
12   assert(a != 0); //May Vs Must
13
14 if(b == 10)
15   assert(v != 0); //O-AEP
16
17 if(x < 100)
18   assert(x < 10); //Insufficiency
19 }
```

- ▶ Is RVCs identification accurate?
  - ▶ *may-cnv* variables Vs *must-cnv* variables
  - ▶ Impact of over-approximation of execution paths (O-AEP)
  - ▶ Insufficiency of *cnv* variables
- ▶ Define two parameters
  - ▶ Precision =  $\frac{\text{number of *correctly* identified RVCs}}{\text{total number of identified RVCs}}$
  - ▶ Recall =  $\frac{\text{number of *correctly* identified RVCs}}{\text{number of actual calls violating the assertions}}$

# Effect on False Positives Elimination

- ▶ Define two parameters

- ▶ Time saving =  $1 - \frac{\text{Time taken in } eFPE}{\text{Time taken by } FPE_{orig}}$

- ▶ Elimination loss =  $1 - \frac{\text{False positives eliminated in } eFPE}{\text{False positives eliminated by } FPE_{orig}}$

- ▶ Trade-off

- ▶ Precision Vs Recall
  - ▶ Time saving Vs Elimination loss

## Experimental Set up

- ▶ *cnv* variables computation - implementation in TCS ECA<sup>2</sup>
- ▶ CBMC as the model checker<sup>3</sup>
- ▶ Two applications (50 KLOC and 40 KLOC)
- ▶ Two properties (DZ and AIOB)
- ▶ False positives elimination in three settings
  - ▶  $FPE_{orig}$ ,  $eFPE_{may}$ , and  $eFPE_{must}$
- ▶ Each setting with two context levels
  - ▶  $maxCCL = 1$ , and  $maxCCL = 5$
- ▶ Assumptions
  - ▶ assertions are reachable
  - ▶ code is sliced and every slice corresponds to a single assertion

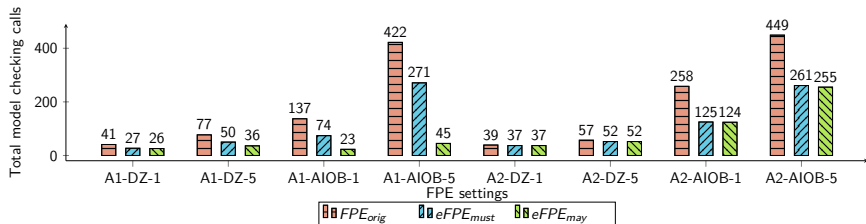
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<sup>2</sup>*TCS Embedded Code Analyzer (TCS ECA)*. .

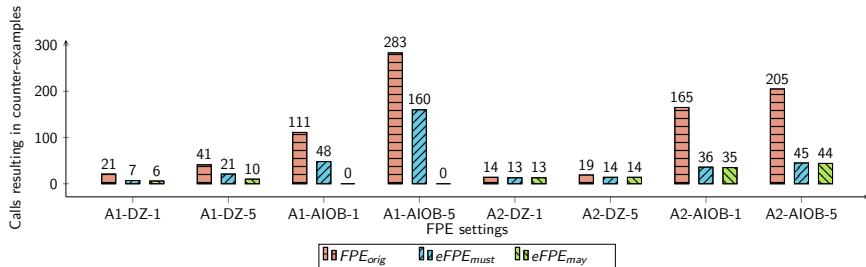
[http://www.tcs.com/offerings/engineering\\_services/Pages/TCS-Embedded-Code-Analyzer.aspx](http://www.tcs.com/offerings/engineering_services/Pages/TCS-Embedded-Code-Analyzer.aspx).

<sup>3</sup>CBMC. <http://www.cprover.org/cbmc/>.

# Experimental Results

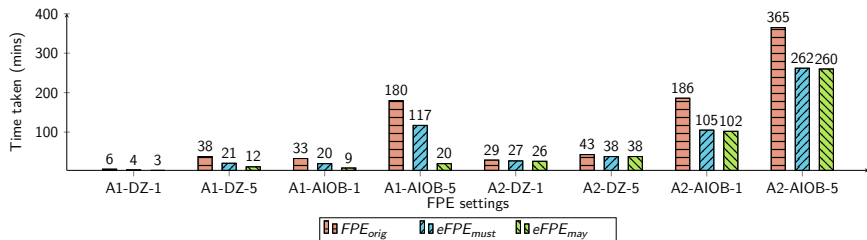


## Total model checking calls

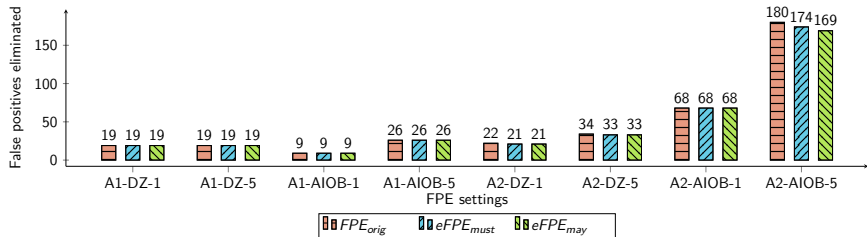


## Model checking calls resulting in counter-examples

# Experimental Results



## False positives elimination time



## False positives eliminated by FPE settings

# Observations

- ▶ RVCs identification
  - ▶ 76% recall with 97.3% precision
  - ▶ Total model checking calls reduced by 49.49%
  - ▶ Calls resulting in counterexamples
    - ▶ original-FPE=58%, efficient-FPE=31%
- ▶ False positives elimination time reduced by 39.28%
  - ▶ Missed elimination of 21/754 false positives
- ▶ Trade-off:
  - ▶ Efficiency 39.28% Vs Elimination loss 2.78%
  - ▶ Failed cases require manual reviewing
- ▶ Constraining over One Vs All assertion variables
  - ▶ both choices applicable in practice



# Summary

- ▶ The problem
  - ▶ large number of model checking calls
  - ▶ poor performance in false positives elimination
- ▶ Our Solution
  - ▶ Introduced a concept of *cnv* variables
  - ▶ Identification of redundant verification calls
- ▶ Experimental evaluation
  - ▶ Trade-off: Efficiency (39.28%) Vs Elimination loss (2.78%)
  - ▶ Spectrum of trade-offs
    - ▶ May Vs Must *cnv* variables
    - ▶ Single Vs Multiple variables
  - ▶ Choice depending on requirement in practice

# Thank you!

Questions or Suggestions?

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